

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Martin Behringer et al. Art Unit : 2811
Serial No. : 09/787,186 Examiner : Shouxiang Hu
Filed : May 17, 2001
Title : SEMICONDUCTOR COMPONENT CONTAINING LATTICE MISMATCHED
SEMICONDUCTOR MATERIALS

Mail Stop RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR THREE-MONTH EXTENSION OF TIME

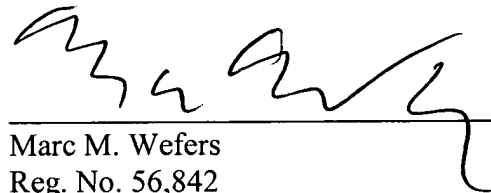
Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated July 19, 2005, be extended for three months to and including January 19, 2006.

Enclosed is a check for \$1020 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050, referencing 12406-018001.

Respectfully submitted,

Date: _____

1/19/06



Marc M. Wefers
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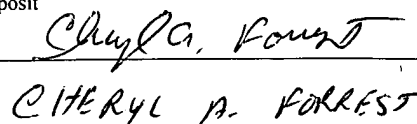
CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

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Signature



C17ERYL A. FORREST

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